
Pulse And Digital Circuits By Yoganarasimha Pdf

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Voltage pulse generation by capacitor and r,c circuits, Pulse wave shaping using voltage-controlled crystal oscillator, Pulse shaping using RC network, Complete passive pulse wave shaping circuit, Pulse wave shaping circuit through diodes, Resistor networks and their applications in pulse wave shaping, Bistable circuits, Op amp driven switch-mode power supply, Design and implementation of voltage chopper circuit using MOSFETs, Circuit for pulse weighing and averaging, Op amp comparators design and application. Voltage Pulse Generation By Capacitors And R,C Networks | Analog Circuits | Circuit Design And Implementation Of A Voltage Chopper Circuit By Using MOSFETs | Switching Characteristics Of Devices With A Narrow-Line Bias Voltage | Pulse Wave Shaping Through A Voltage Controlled Crystal Oscillator | Pulse Wave Shaping With Passive R,C Networks Many fundamental operations of circuits are performed by combinations of blocks called arithmetic circuits, which can be combined to build higher-level circuits, such as counters, pulse generators, oscillators, and lookup tables. The arithmetic circuit is a collection of nonlinear logic gates that implement the arithmetic operations of addition, subtraction, multiplication, and division on a set of single-bit inputs, typically a pair of bits representing two numbers (numbers), the binary representation of a third number (sign, absolute value), or the values of two signals. The most important example of a two-port arithmetic circuit is the adder, which adds the values of its input ports. (This is a little-known fact, because the term has become synonymous with an inverter). It is important to note that

the operation of an arithmetic circuit does not always imply a digital computation, that is, the production of a single output, and not simply the evaluation of a relation between two input signals. An important example of an arithmetic circuit, well known in digital computers, that does not have a digital operation is the carry-lookahead circuit.

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The overall goal of our research effort is to better understand the functional contribution of the mossy fiber pathway. The first project we initiated was to map the firing field of the MF axons. This provided initial information about the regions in which mossy fibers make connections, what the mossy fiber response characteristics are in different regions and whether the responses can be shaped by synaptic plasticity (Courtine et al. 1998). The second research goal was to understand the functional contribution of mossy fiber activity on each of the circuits in which it is synaptically connected. This requires selective stimulation of the mossy fiber axons. To achieve this, an additional microelectrode is required to electrically isolate the axon from the dendrite where the synapse is located. This electrode is called the axon block. We used a standard tetrode to perform the isolation of the axon in a functionally isolated pathway. Now we performed the mossy fiber isolation in the CA3 circuit. We have established that the normal and conditioned responses in the CA3 circuit after MF stimulation can be altered by the MF axon blockade. The time course of the blockade is slow, allowing more time for the plasticity to occur. This was controlled by the Ca²⁺ antagonist Nifedipine. The blockade could also be reversed. We examined the plasticity in the dentate gyrus circuit by 2 min long conditioning stimulation using HFS in Control and DCS experiments. The results revealed that dentate gyrus,

which is not connected to mossy fibers, exhibited plastic changes in the response size following stimulation of the CA3 circuit (Sutula et al. 2007). 5ec8ef588b

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